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## NEPTUN cod:

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## I+C Technology

First Exam, ${ }^{\text {st }}$ Semester: 2016/2017
28.10.2016
1., Simplify the following Boolean expression using Boolean rules. Convert the simplified expression into the canonical form. $\left(A=2^{3}, B=2^{2}, C=2^{1}, D=2^{0}\right)$

$$
F=\overline{\overline{A+B \bar{C}}+D(\overline{A+\bar{B}})}
$$


(6 pts)
2., Given is a logic circuit below. Write a possible combination of input values, when the output $F$ is 1 and input A is 0 . Write a combination of input values if you can change only one input and the output $F$ alters to 0 .


Solution

$$
\begin{gathered}
F=1, \text { if } A=0, B=\_, C=\ldots, D=- \\
F=0, \ldots \text { input change }
\end{gathered}
$$

3., What is the relation between these functions?

$F_{2}=\sum^{4}(0,1,3,4,5,12)$
$F_{3}=C(B+\bar{D})+A(\bar{B}+D)$


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Solution
$F_{1} \ldots F_{2} \ldots F_{3} \ldots F_{4}$
4., Write a Boolean expression for the following circuit (F), then simplify that expression as much as possible. Draw a logic gate circuit using only NAND gates with 2 inputs.


5., Construct a circuit that has 4 inputs (A,B,C,D) and 3 outputs. A four-bit binary number ( $A=2^{3}, B=2^{2}, C=2^{1}, D=2^{0}$ ), appears on the input to a combinational logic circuit. Output $F_{1}$ indicates whether the number is divisible by 2 without any remainder (see row 4 e.g.), output $F_{2}$ indicates if the number is divisible by 5 without remainder (see row 5 e.g.) and output $F_{3}$ indicates if the number is divisible by 2 and 5 too. Obtain the algebra form for $F_{1}$ and $F_{2}$. Draw a circuit diagram for the minimized the $F_{3}$ functions. ( 0 is divisible by any number)

|  | $A$ | $B$ | $C$ | $D$ | $F_{1}$ | $F_{2}$ | $F_{3}$ |  |  |  |  |  |  |  |  |  |  |  |  |  |
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| Solution |  |  |  |
| :--- | :--- | :---: | :---: |
| $F_{1}=$ | $F_{2}=$ |  |  |
| $F_{3}$ |  |  |  |
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